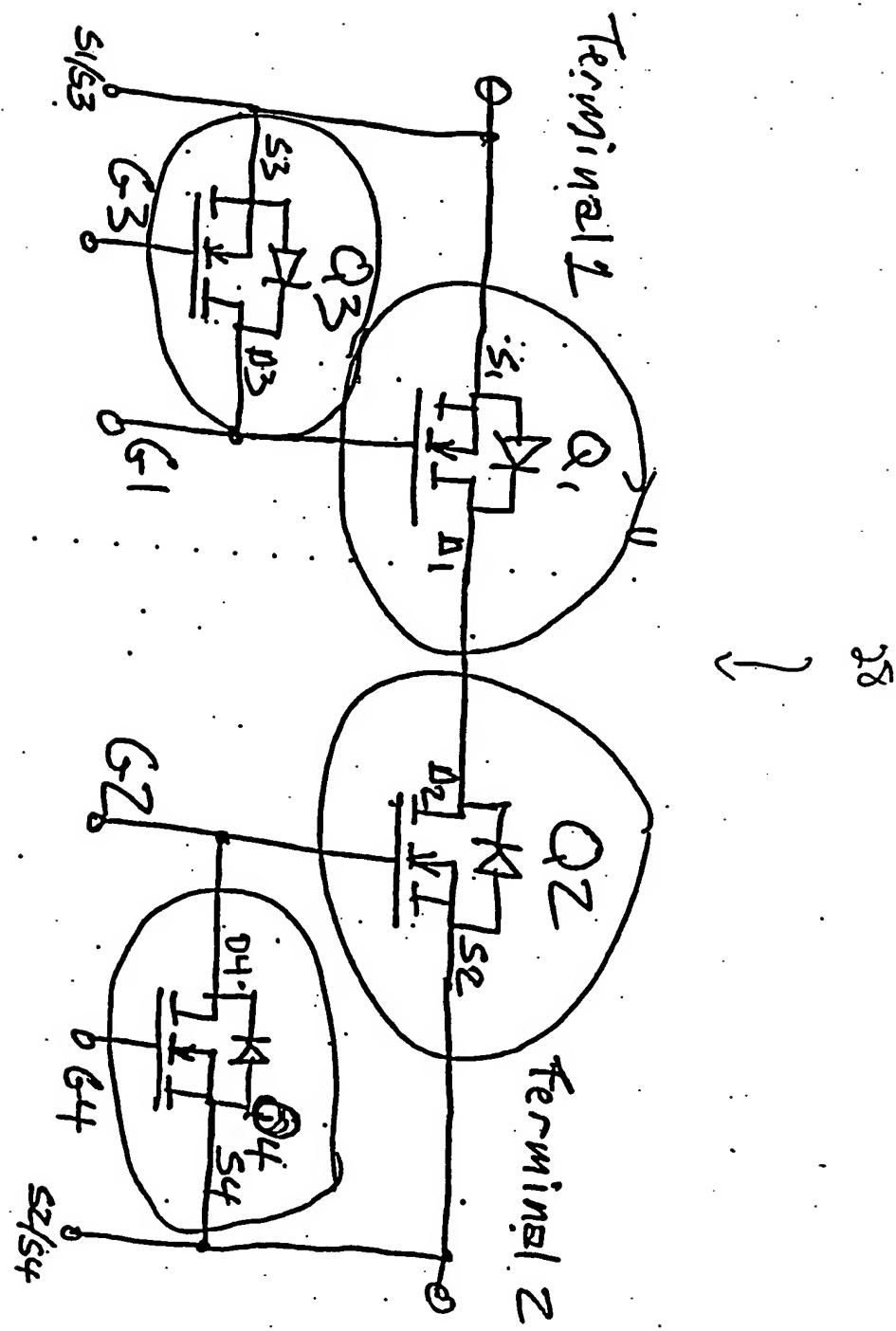


F<sub>16.1</sub>

Fig. 2



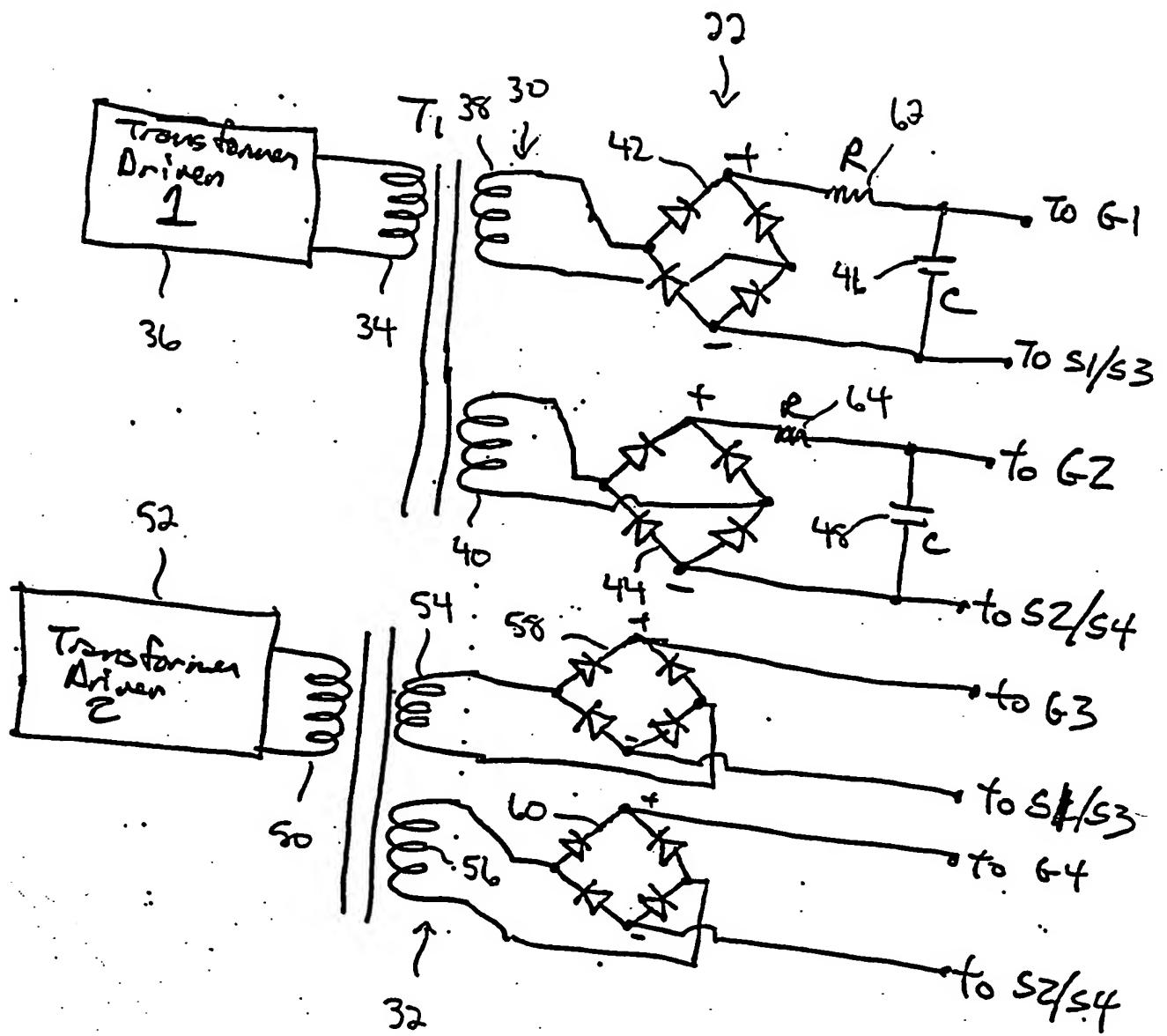
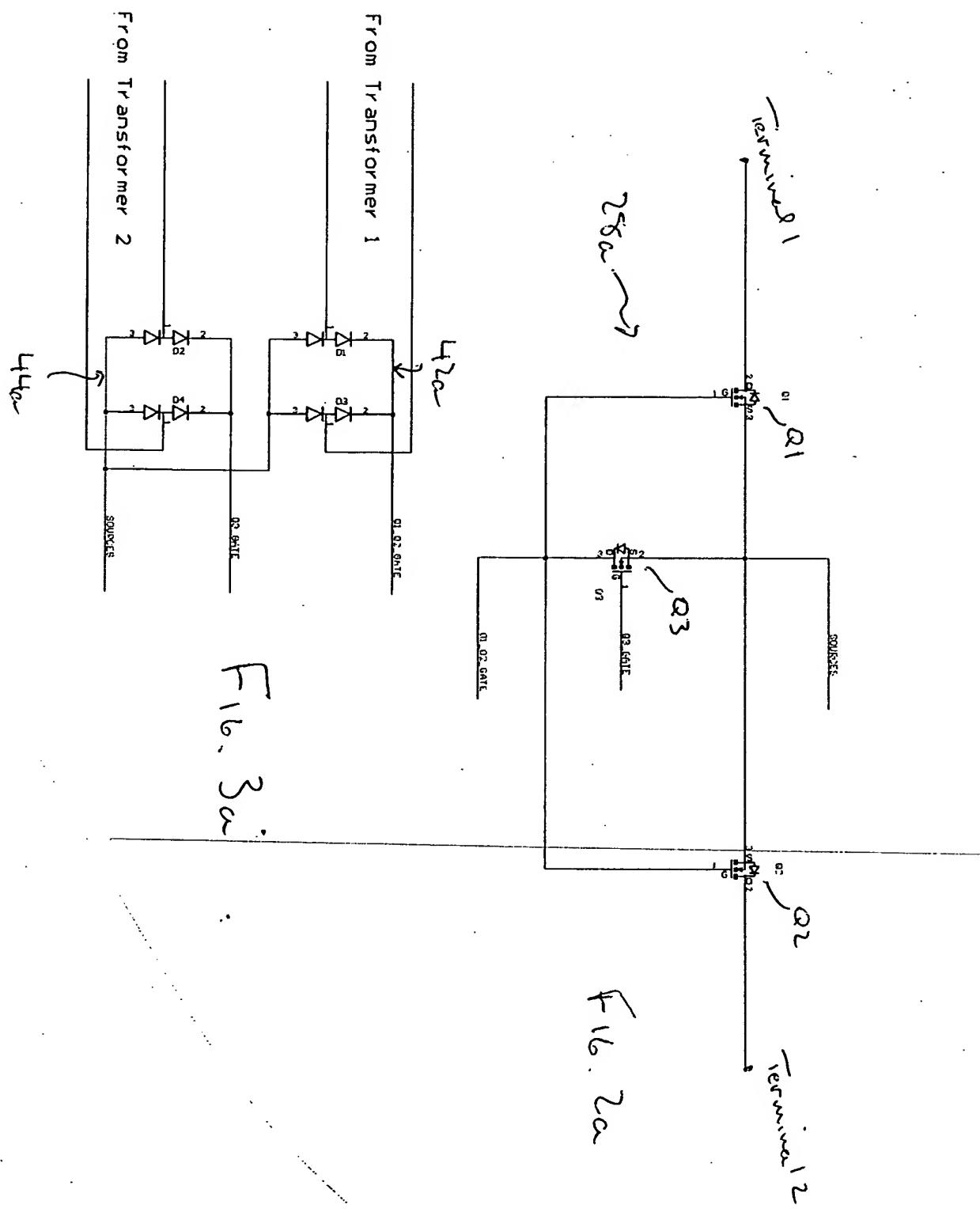
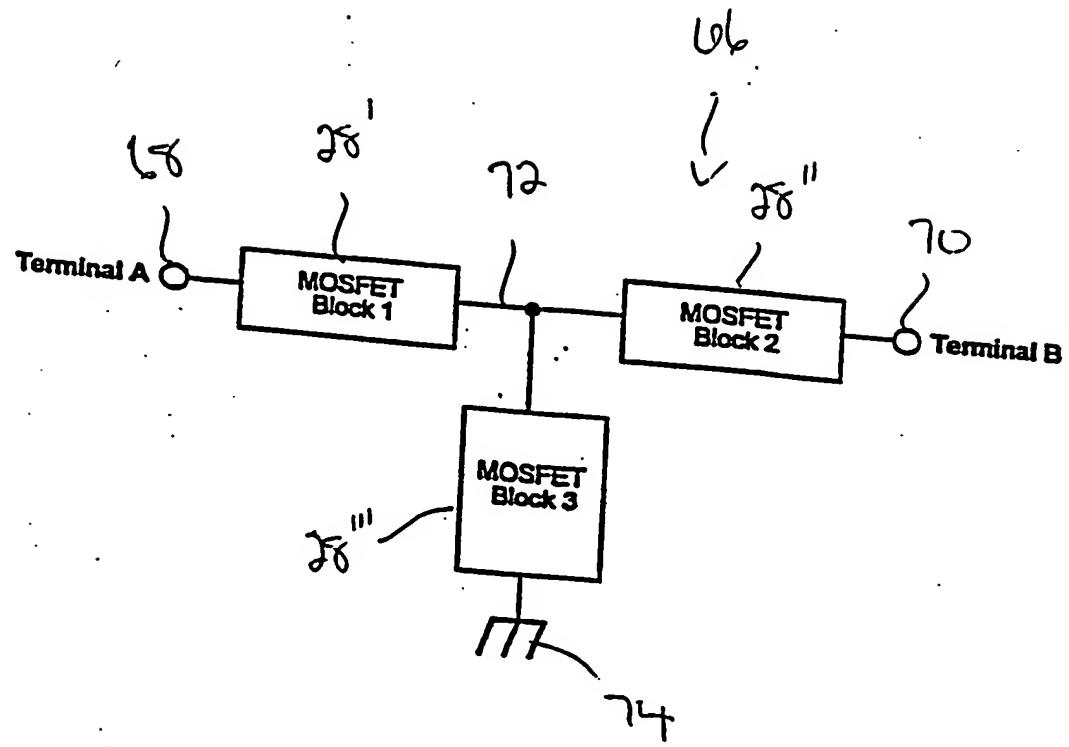


Fig. 3





F<sub>16</sub>. 4

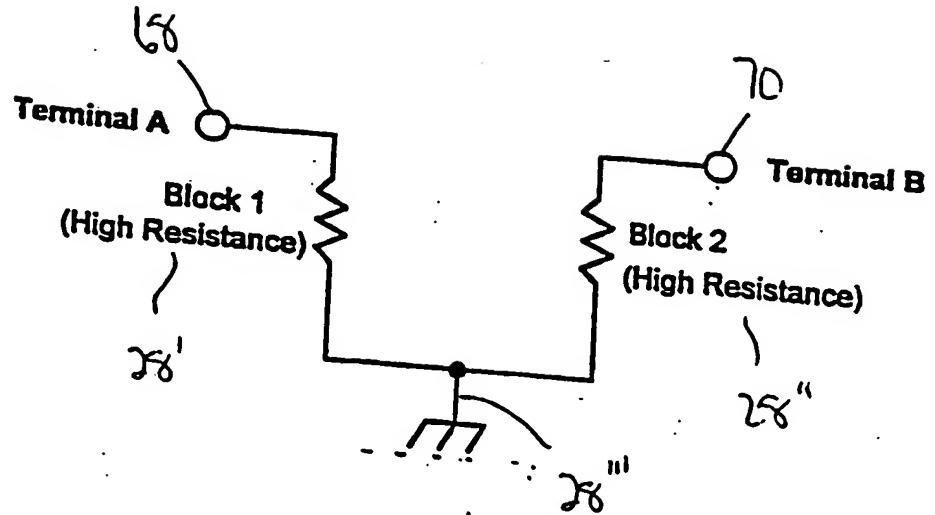


Fig. 5

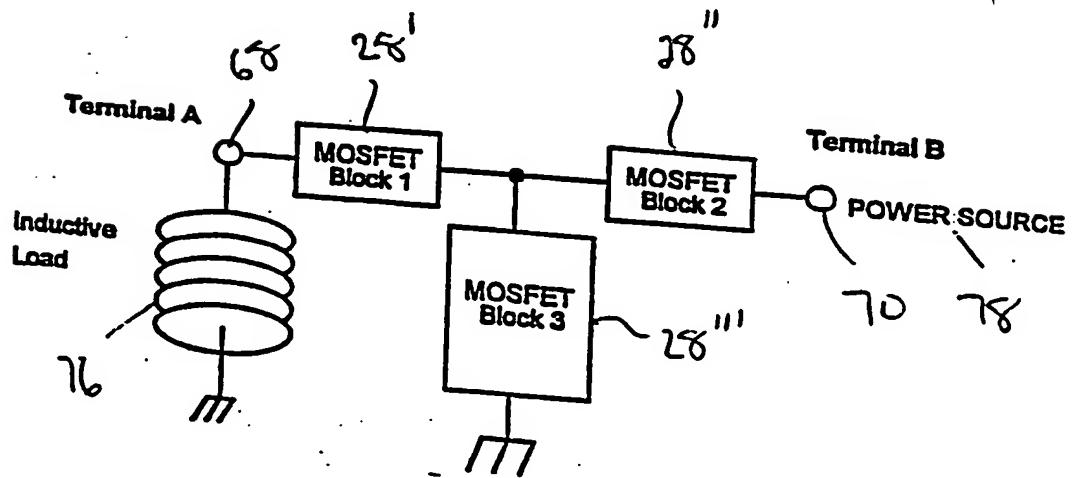
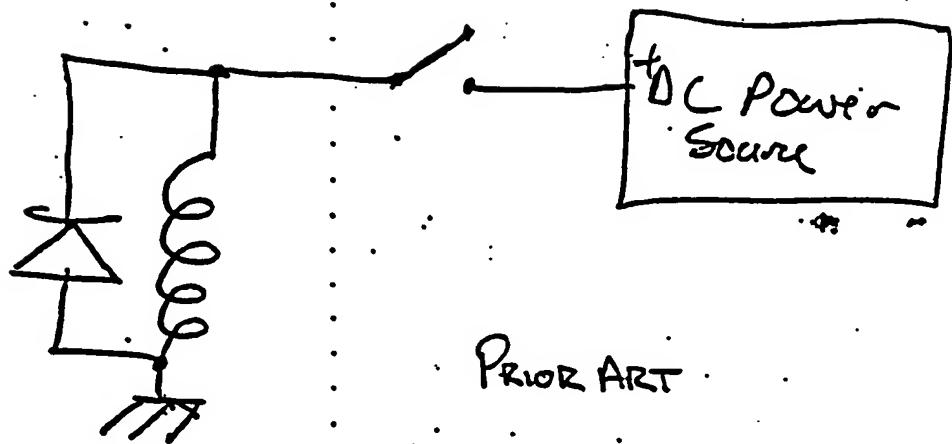


Fig. 6

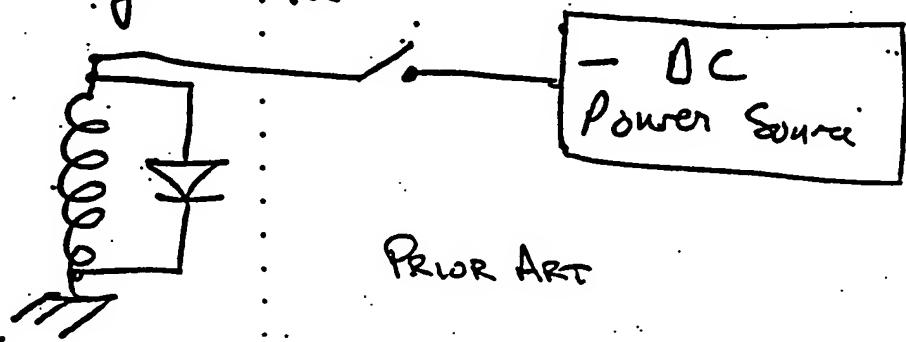
Figure 7



PRIOR ART

Inductive load with Positive power source and discharge (or flyback) diode

Figure 7a



PRIOR ART

Inductive load with negative DC power source and discharge (or flyback) diode.

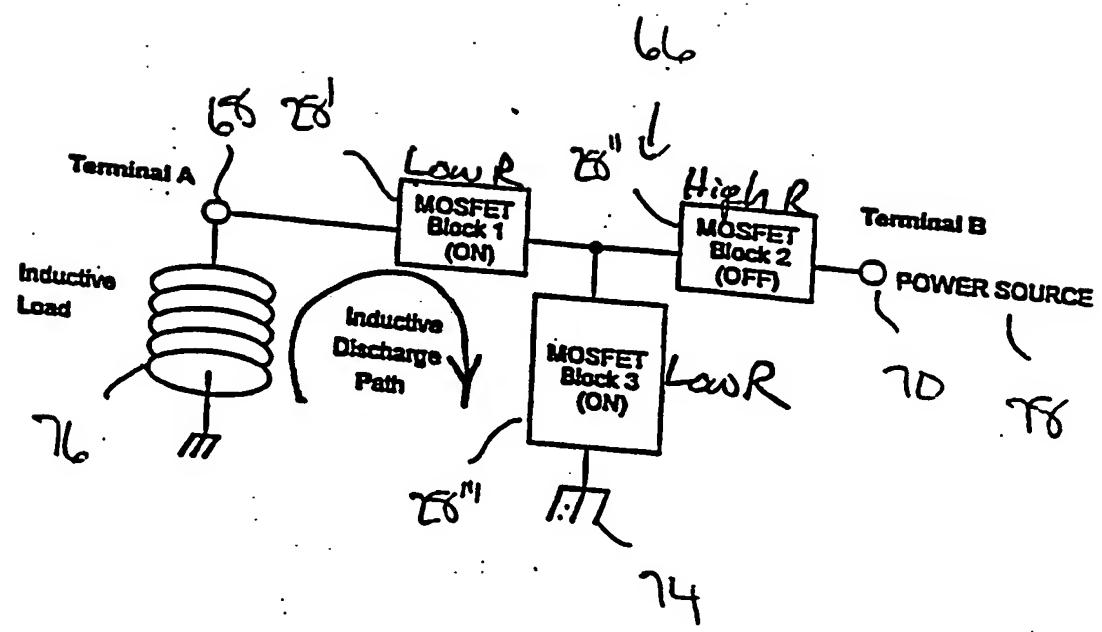
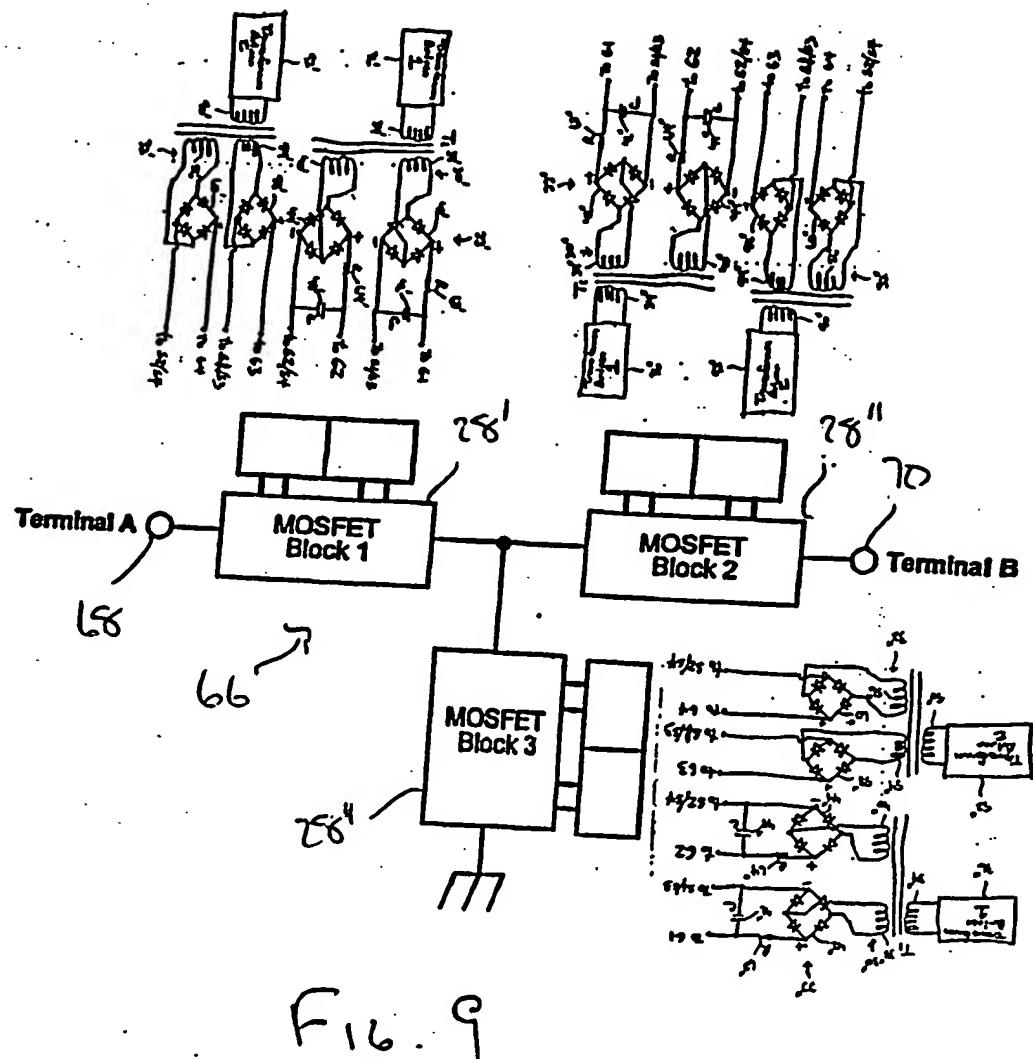


Fig. 8



F16.9

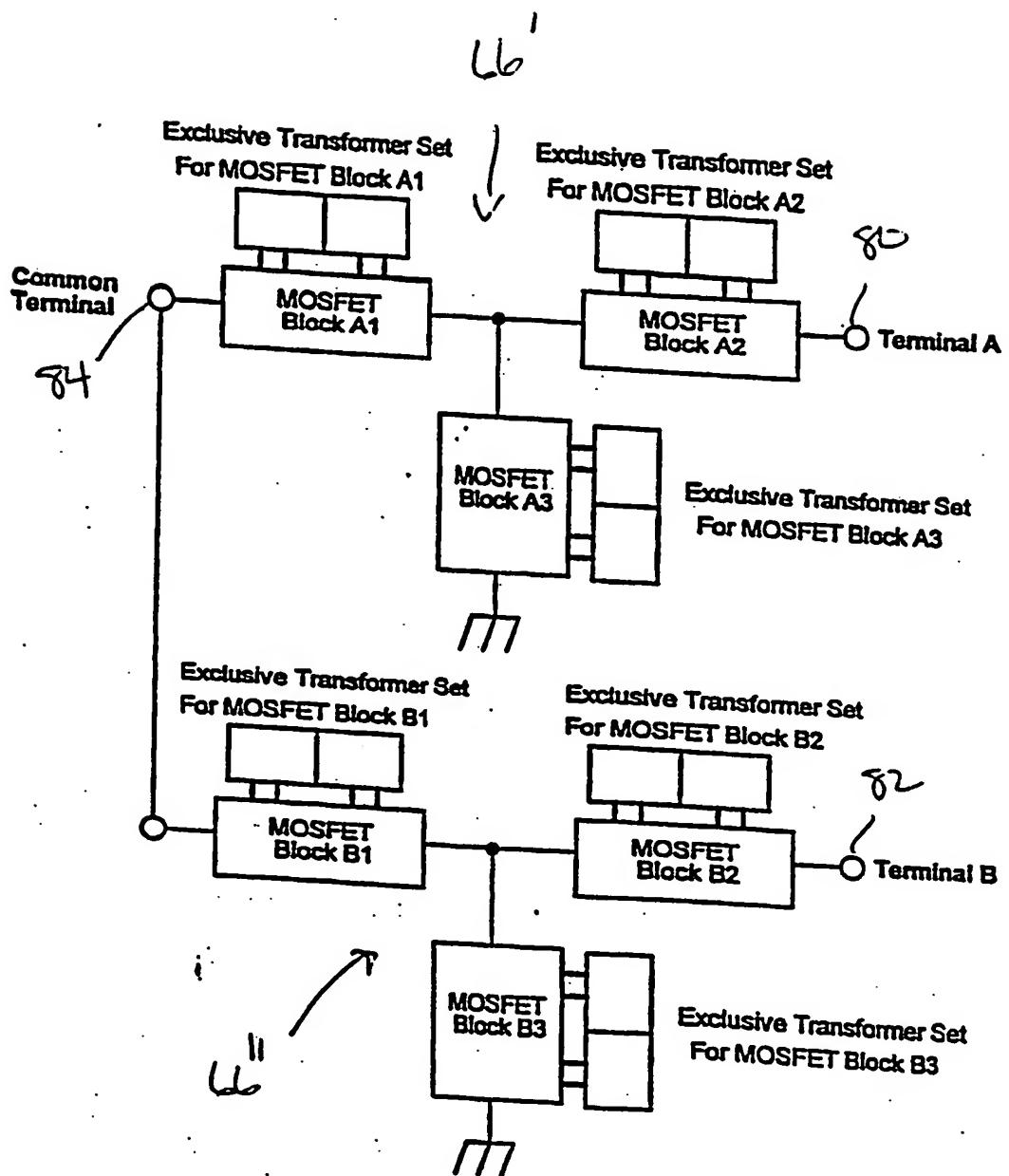


Fig. 10

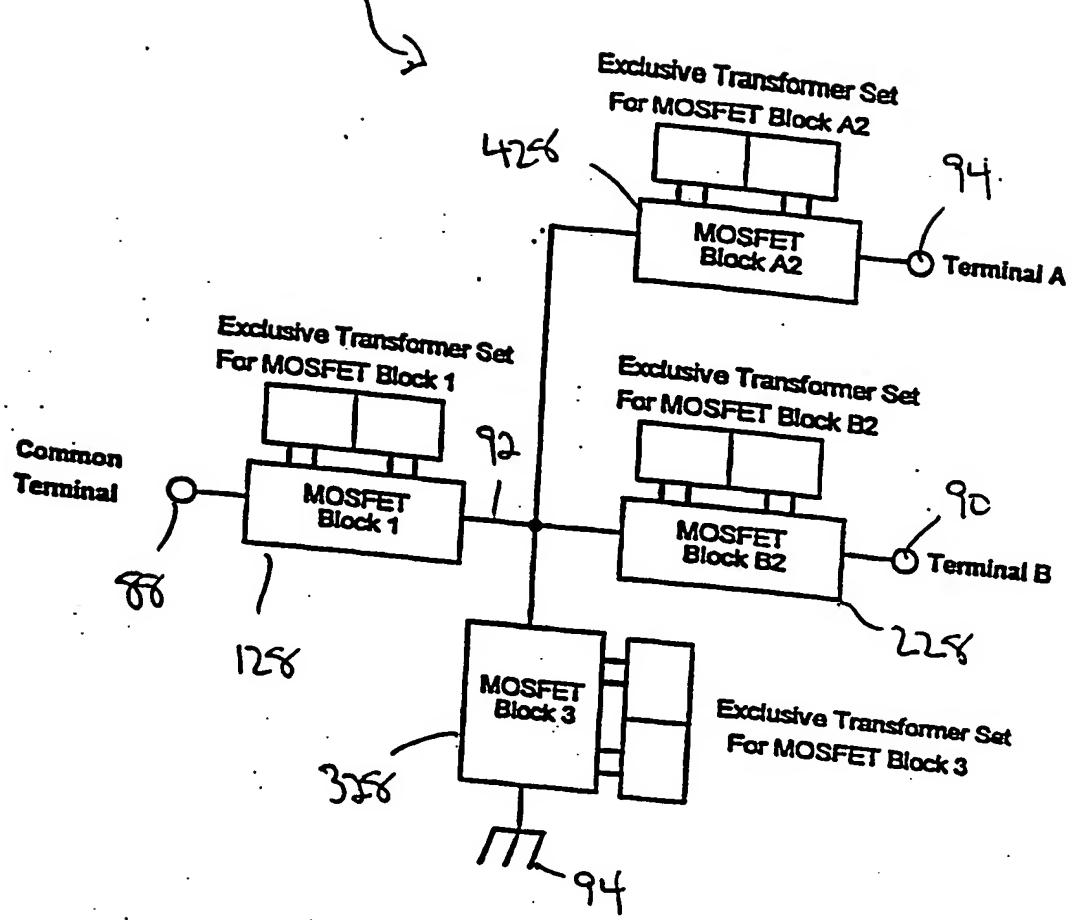


FIG. 11

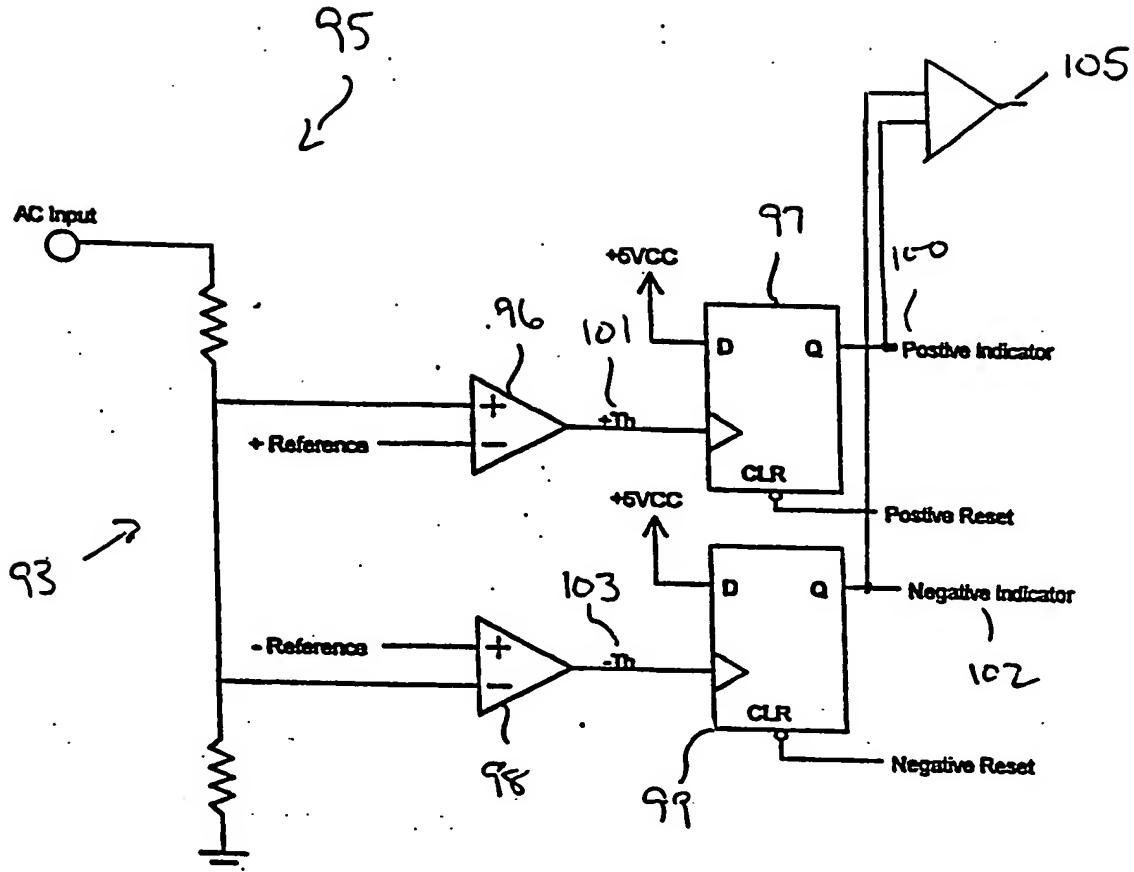
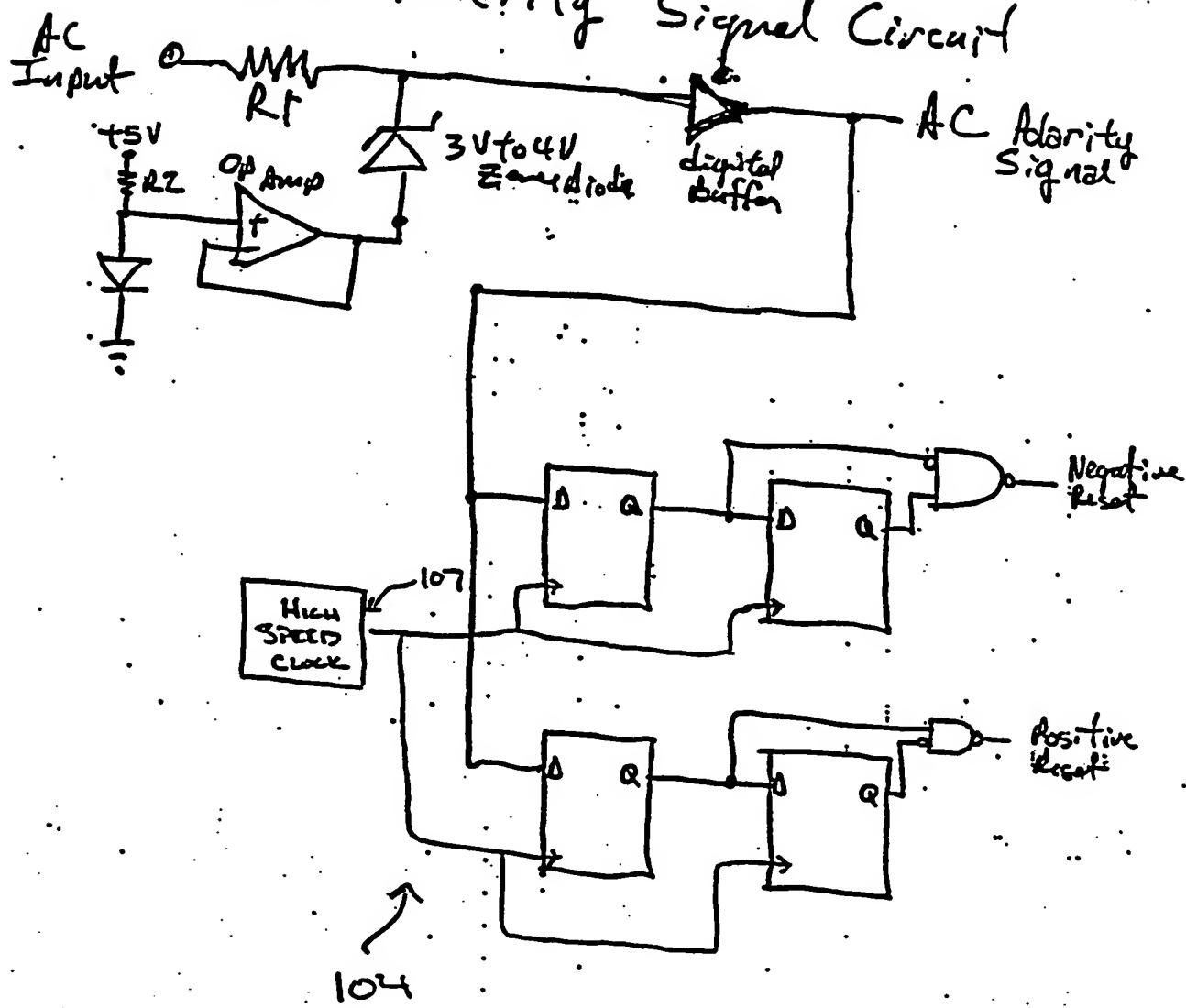


Fig. 12

Figure 13  
AC Polarity Signal Circuit



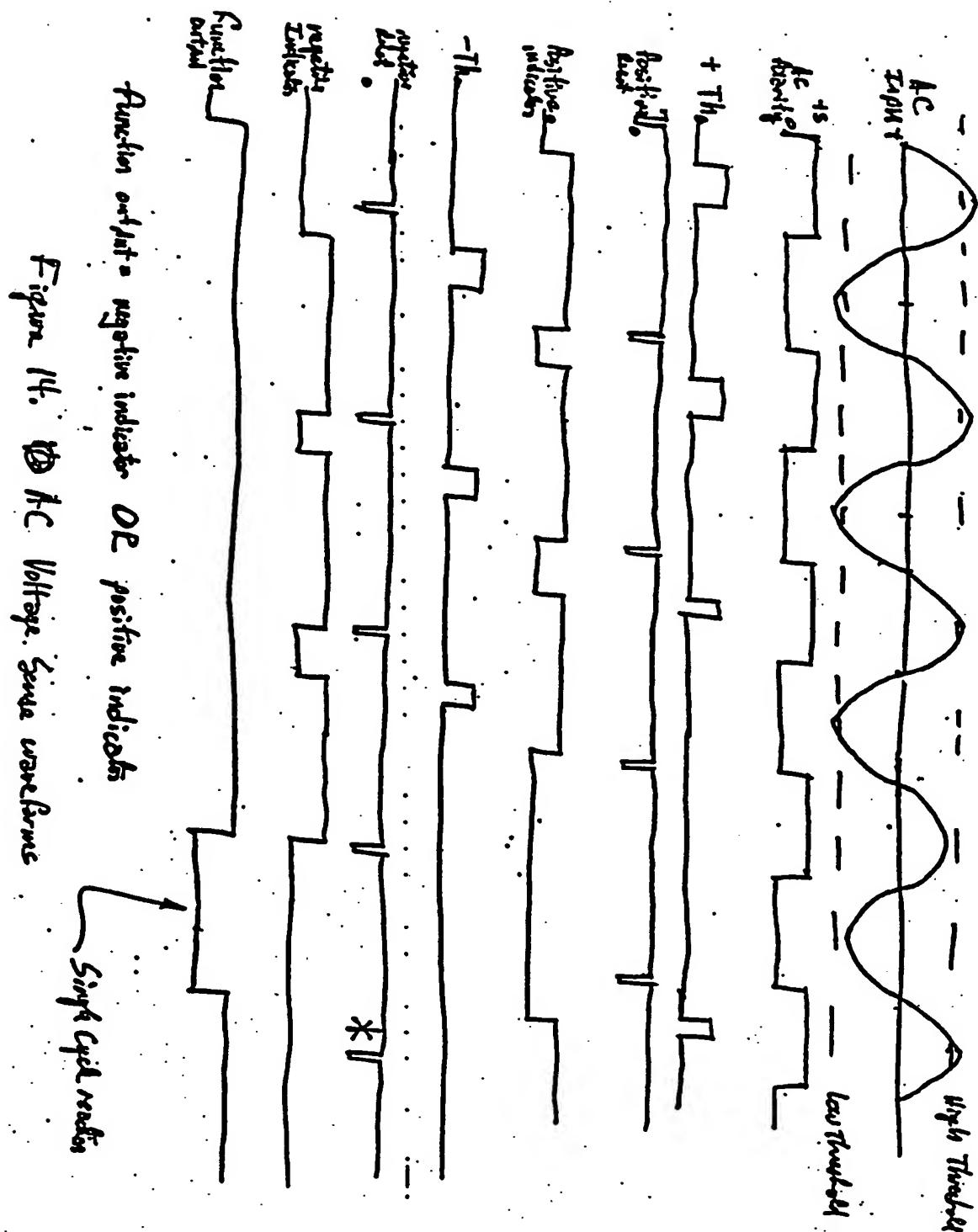
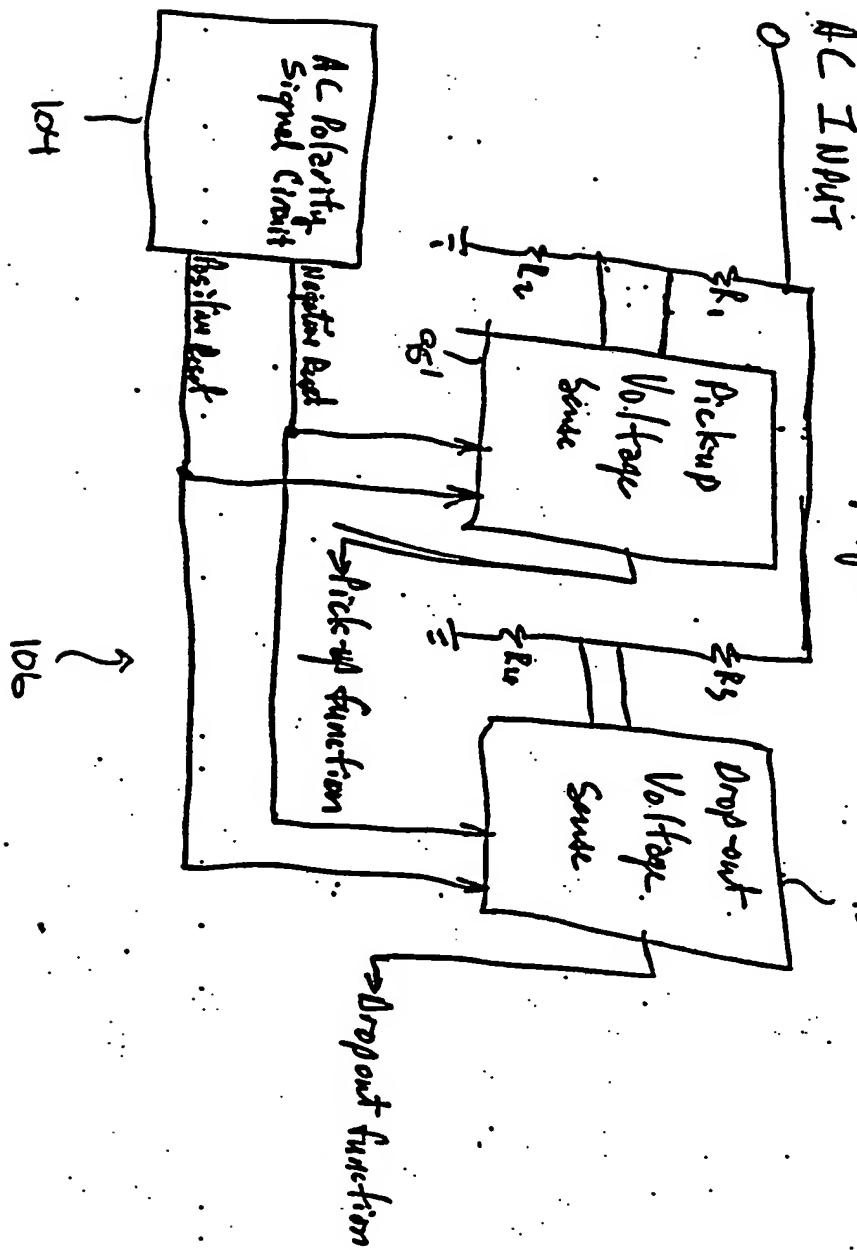


Figure 14. AC Voltage Sense waveform

Figure 15

qs"



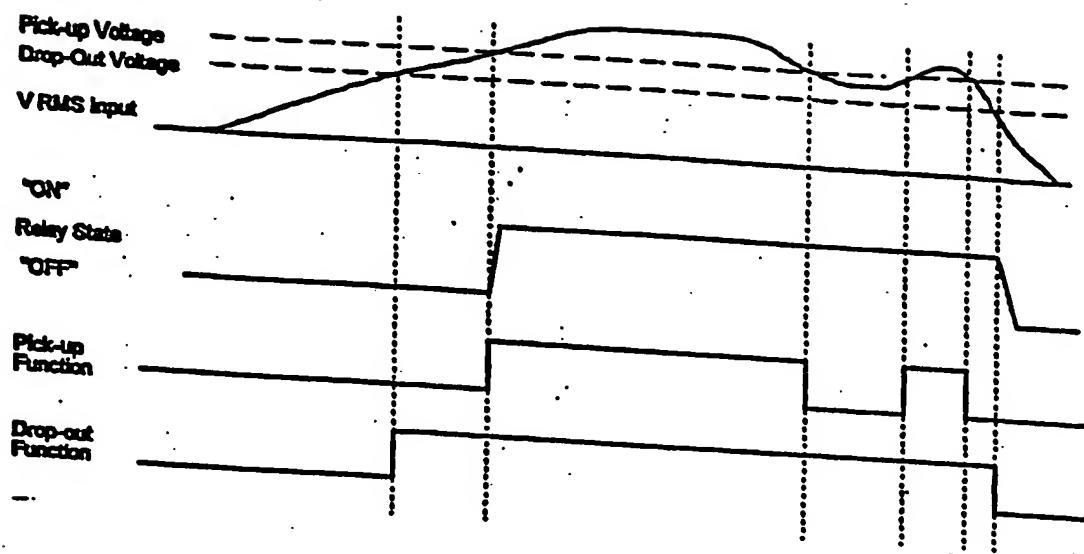
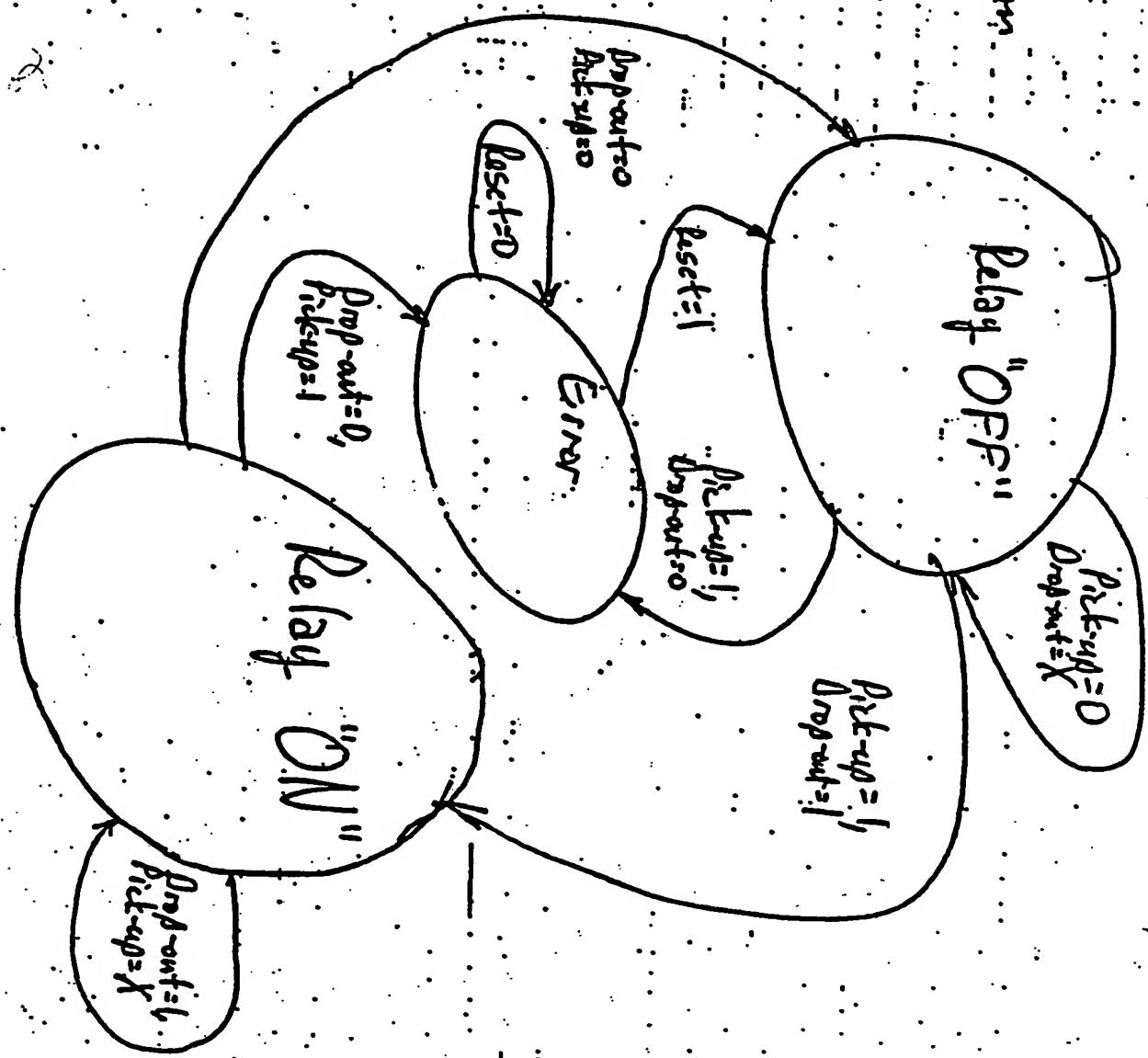


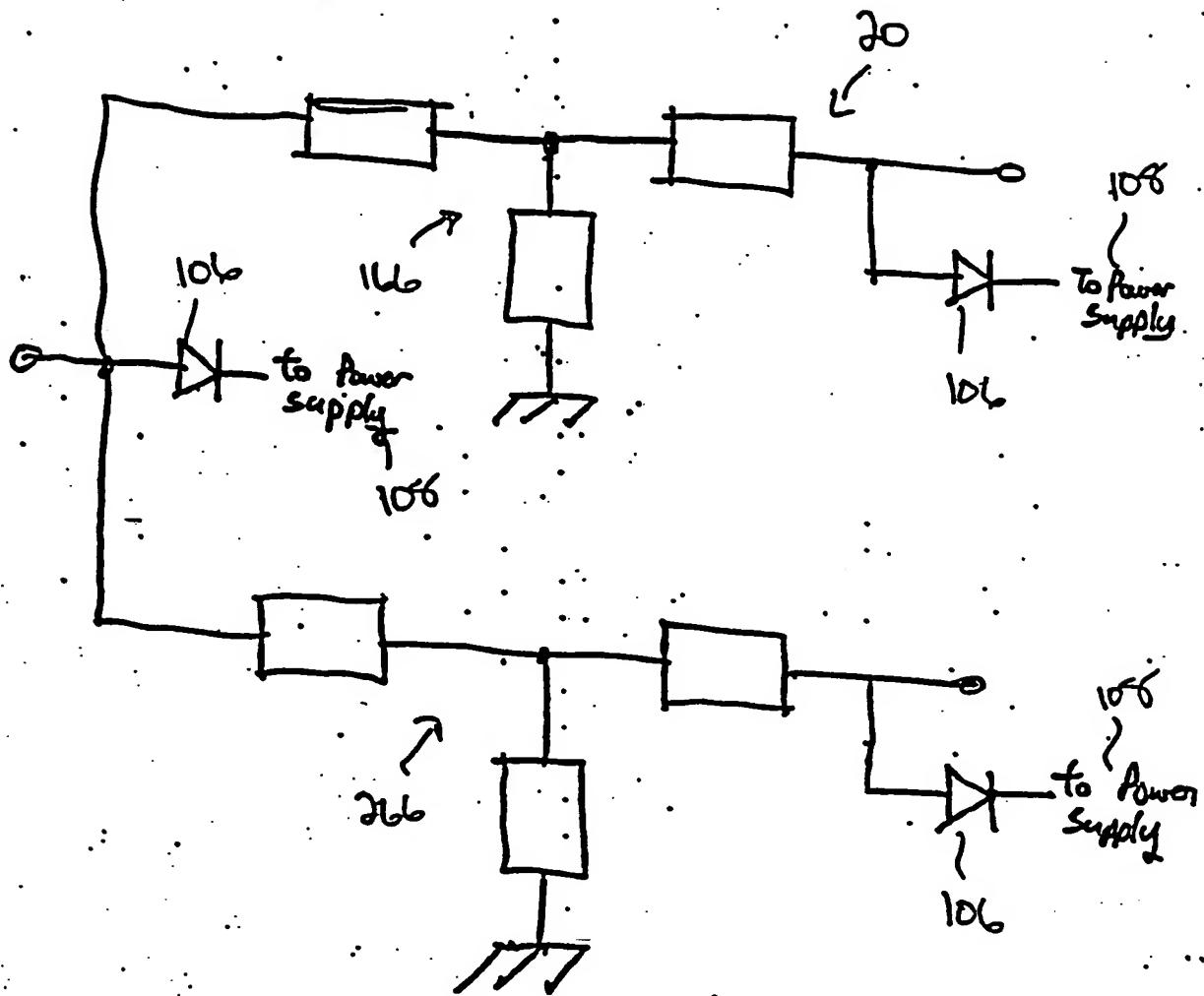
Fig. 16

Figure 17.  
State Diagram



X = "Don't care"

Figure 18



AC SENSE INPUT

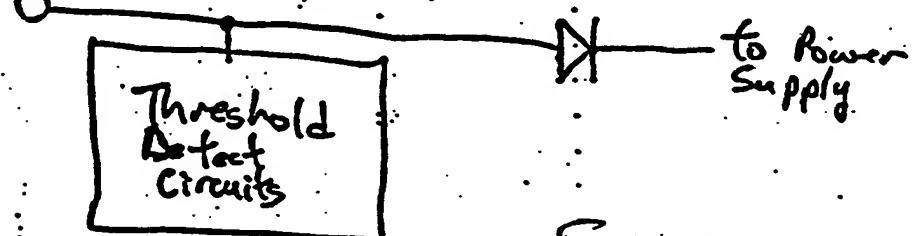
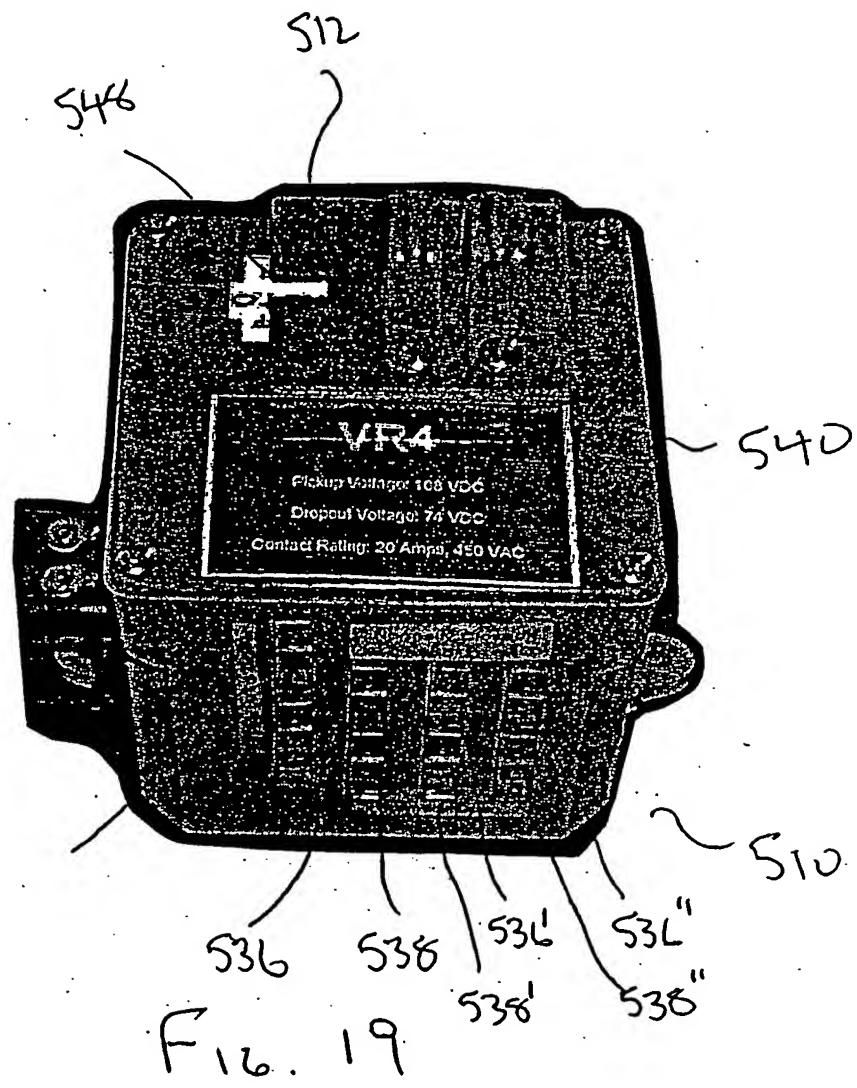
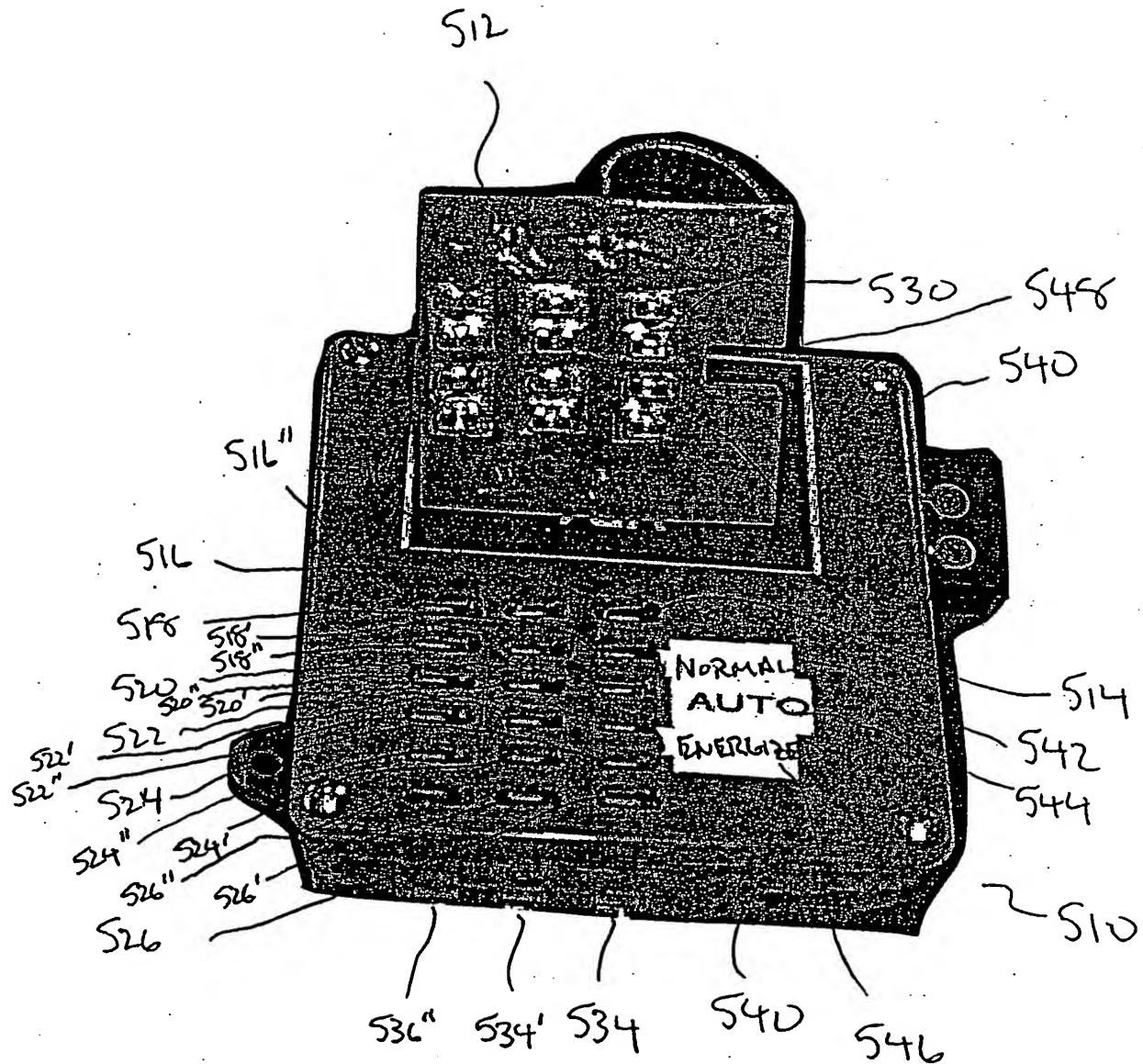
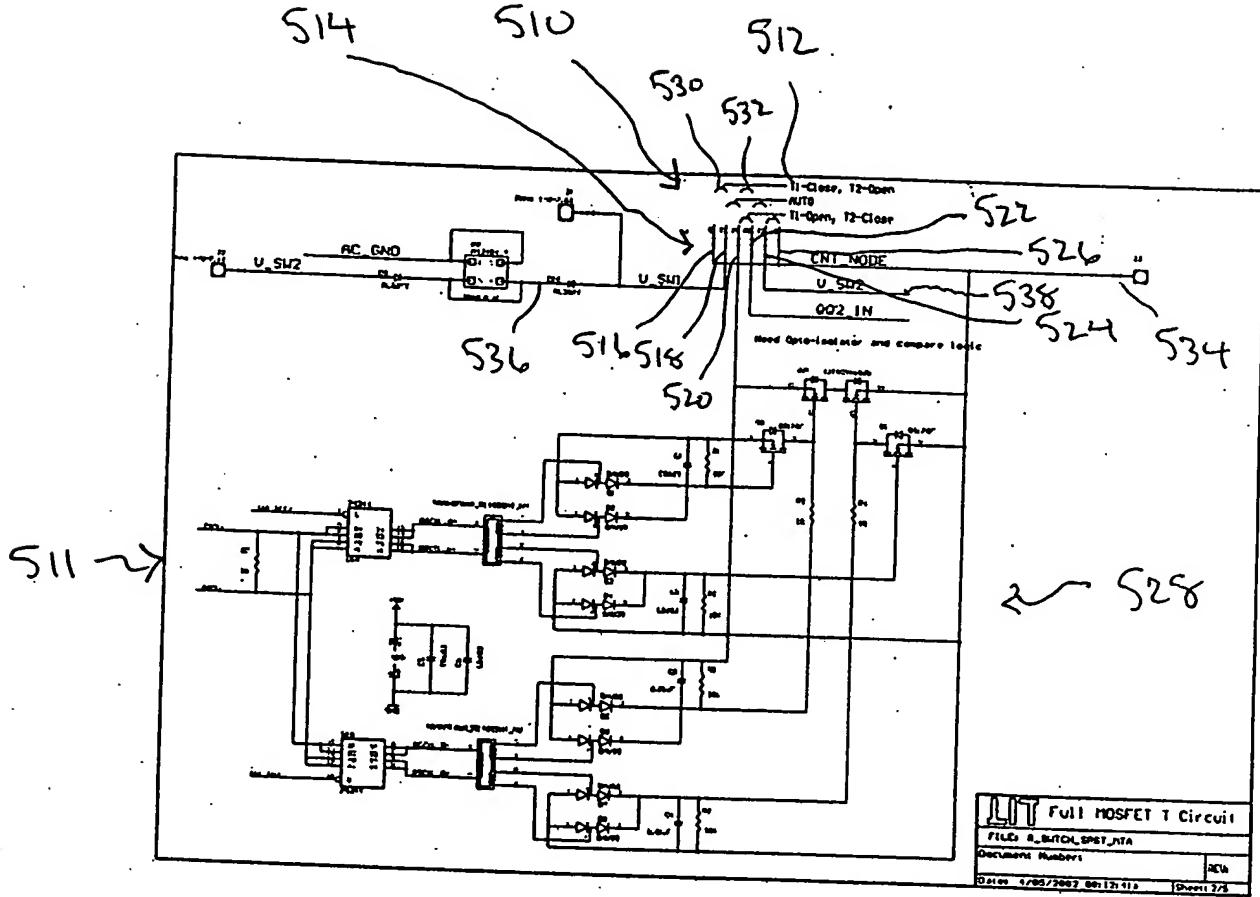


Fig. 18a





F<sub>16</sub>. 20



F.16. 21

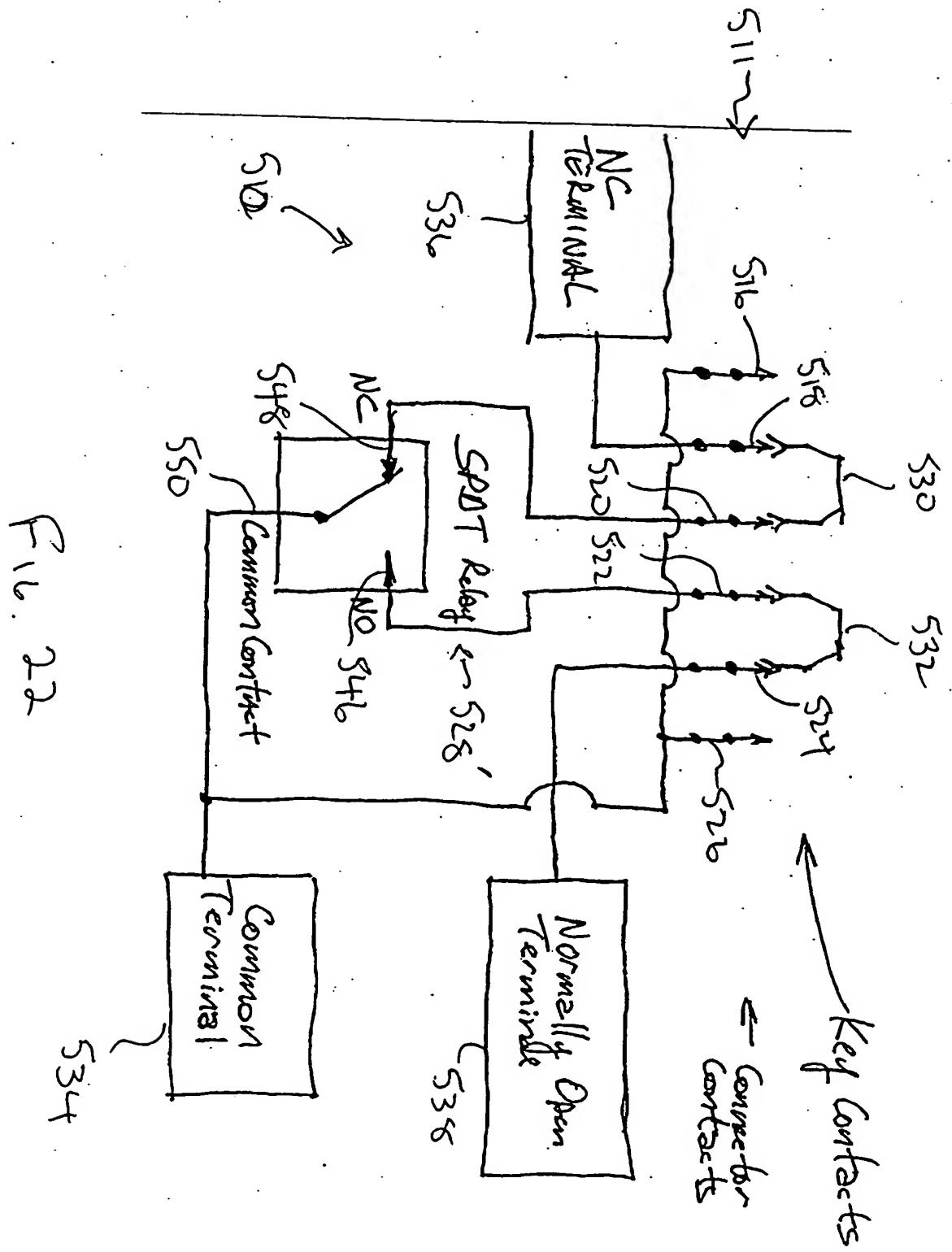
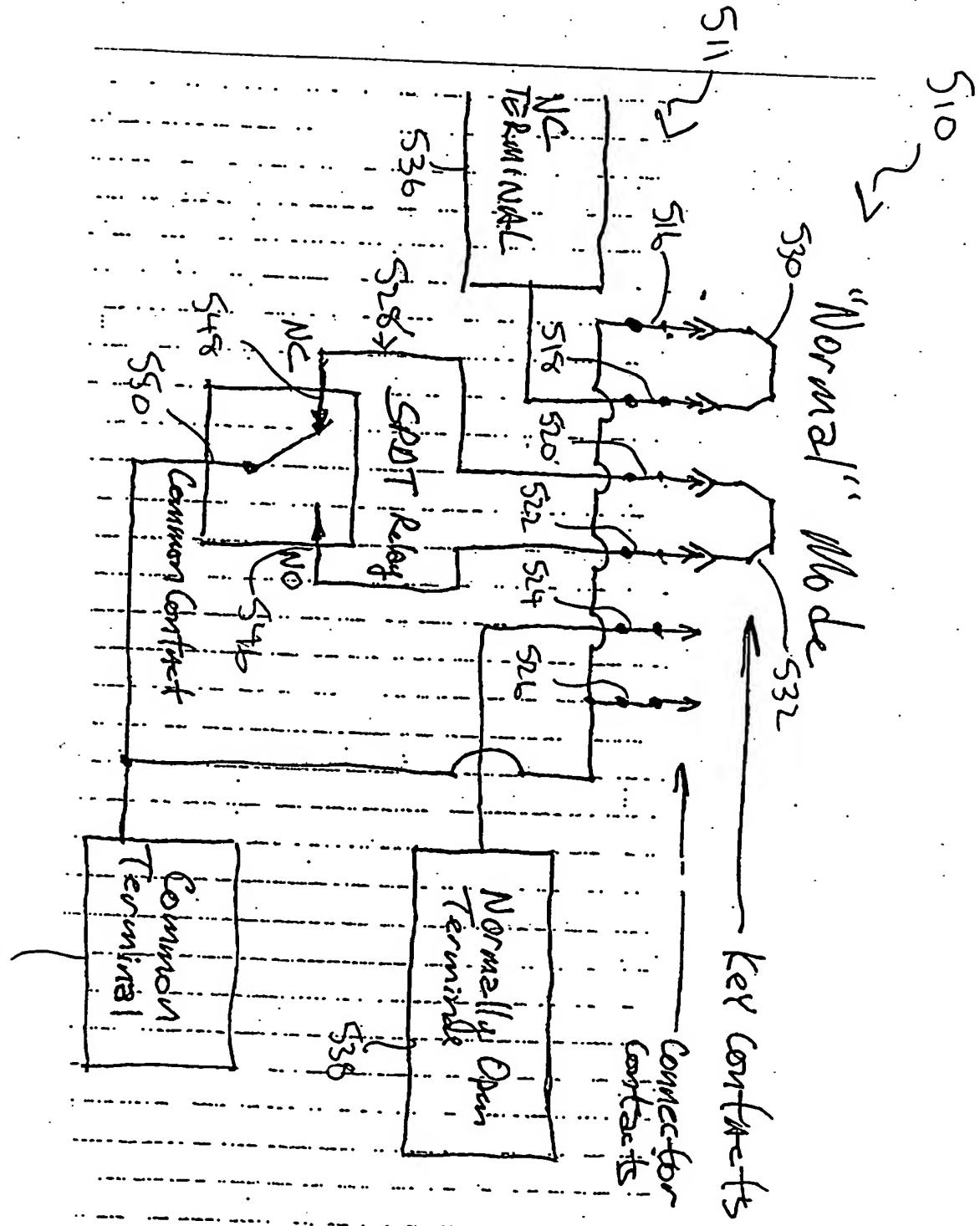
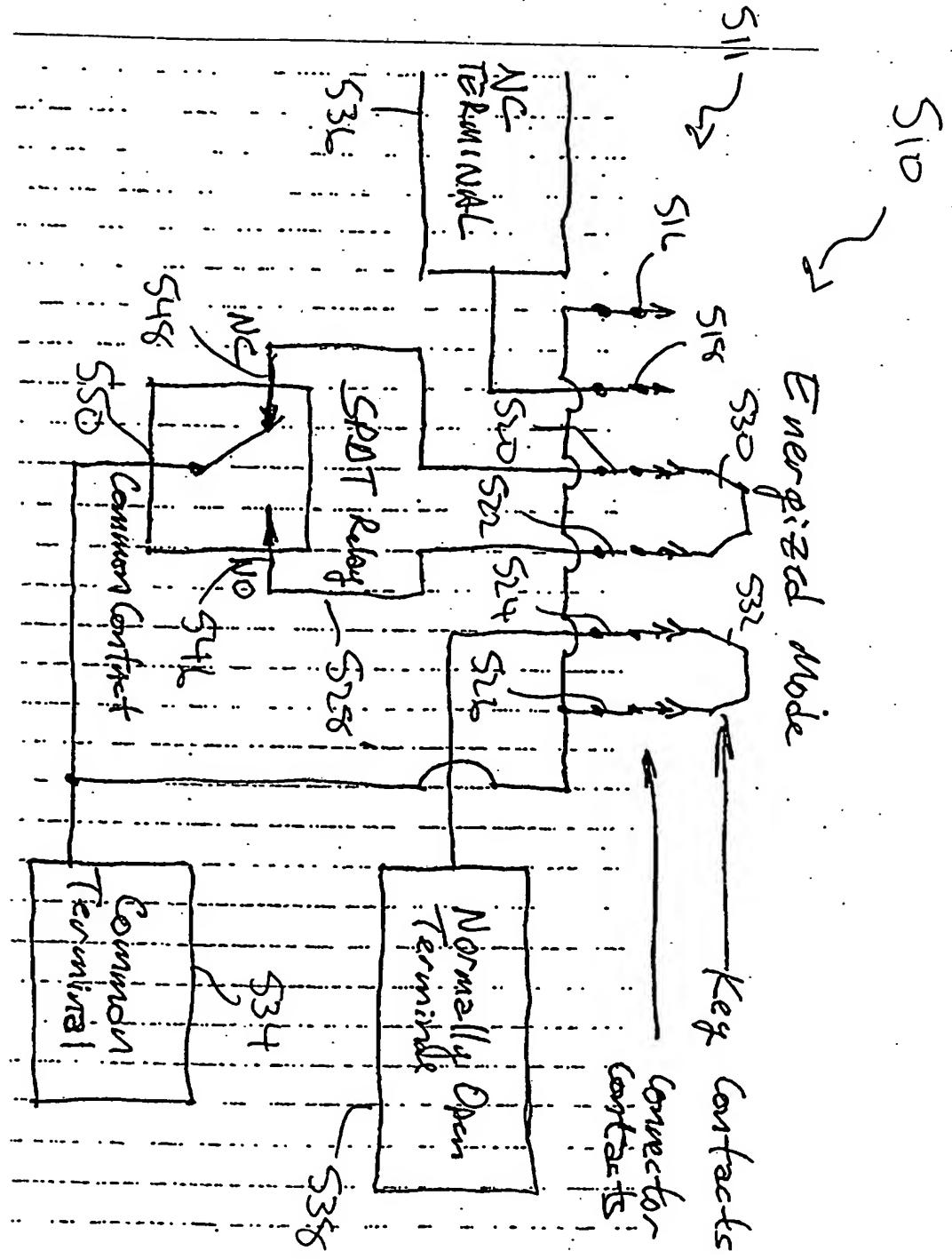


Fig. 22

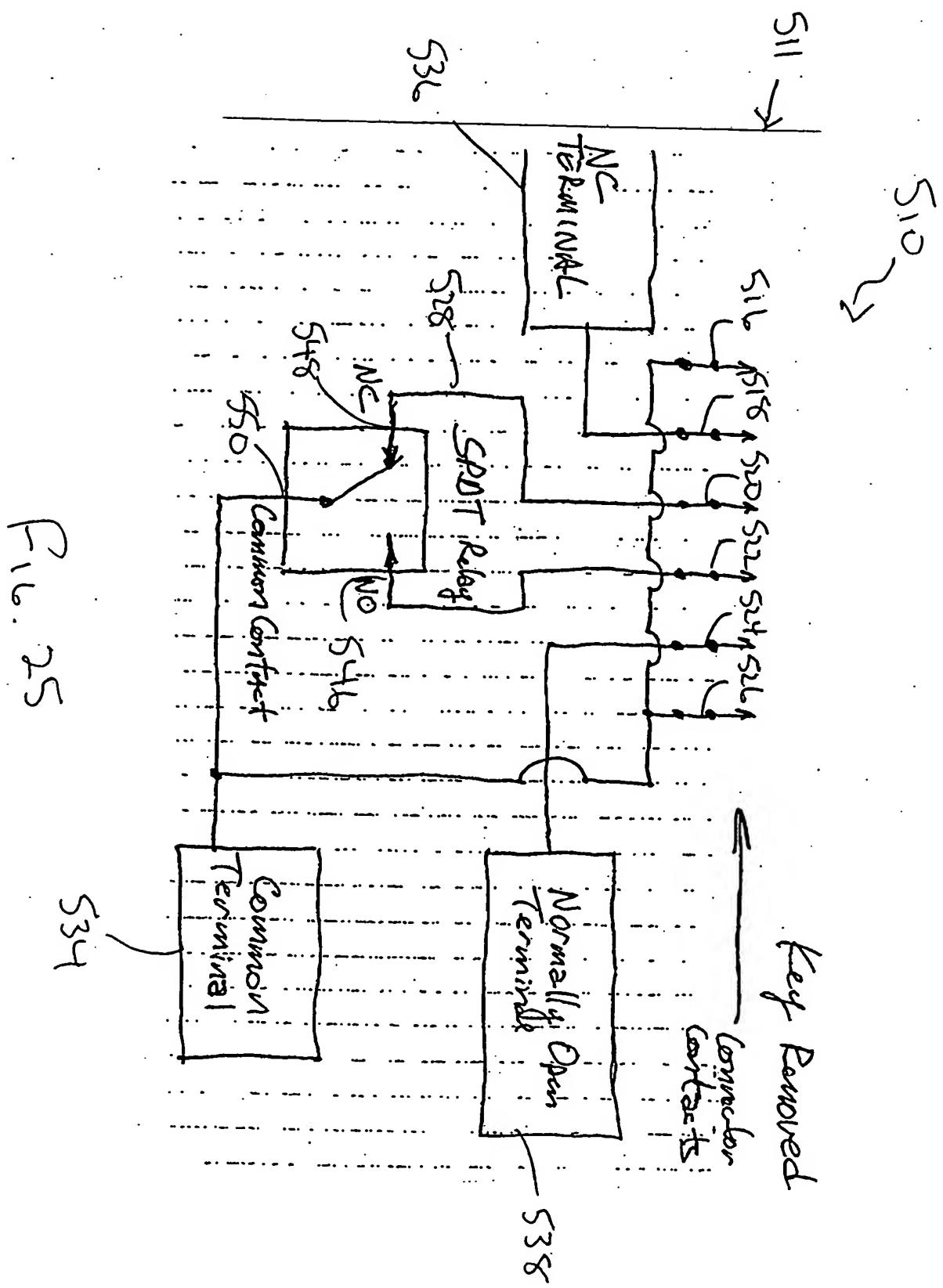


F16. 23

S34

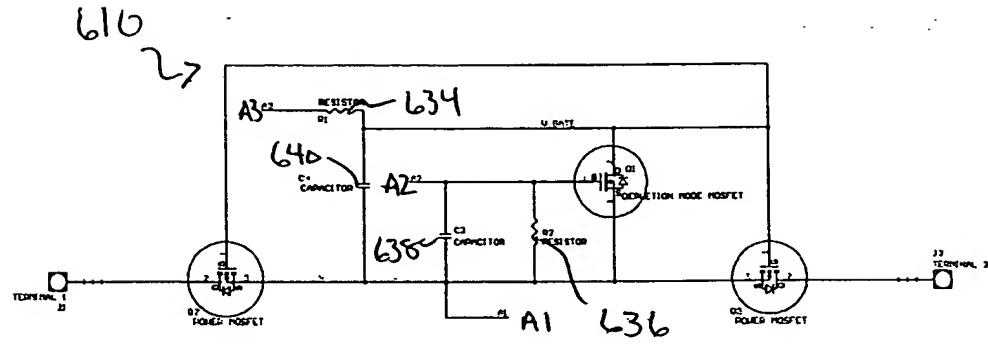


Erg. 24

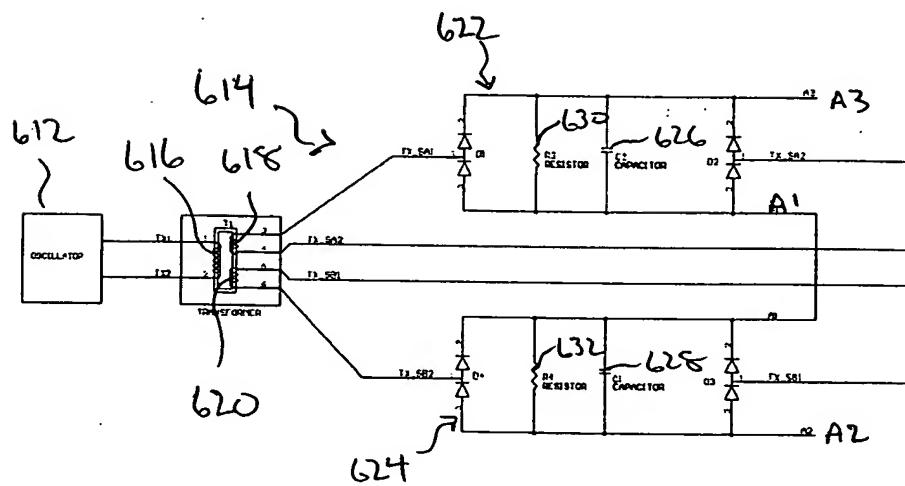


F16. 25

534



F<sub>16.26</sub>



F<sub>16.27</sub>

